

Abstracts

A GaAs High-Speed Counter Using Current Mode Logic

K. Suyama, H. Suzuki, Y. Nemoto, S.I Yamamura and M. Fukuta. "A GaAs High-Speed Counter Using Current Mode Logic." 1983 Microwave and Millimeter-Wave Monolithic Circuits Symposium Digest 83.1 (1983 [MCS]): 12-16.

A single-clocked divide-by-four counter with maximum operating frequency of 3.4 GHz has been developed. The circuit was fabricated using a tungsten-silicide gate self-alignment technique with full ion implantation. The basic building block of the counter current mode logic (CML) master-slave flip-flop similar to Si ECL.

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